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PATENT
3885-0103P**U.S. PATENT AND TRADEMARK OFFICE**

Applicants: Yoshinobu Ono et al. Conf.: 2546
Serial No.: 10/046,741 Art Unit: 2812
Filed: January 17, 2002 Examiner: S. Mulpuri
For: METHOD FOR FABRICATING III-V COMPOUND
SEMICONDUCTOR

DECLARATION UNDER 37 C.F.R. §1.132

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

I, Yoshinobu Ono, do declare and say as follows:

1. I am one of the inventors of the U.S. Patent Application Serial No. 10/046,741, filed January 17, 2002, and I am familiar with the contents of the application, its prosecution before the U.S. Patent and Trademark Office, and the references cited therein.

2. The Background Art section at page 1, line 10 to page 2, line 4 of the specification represents the inventors' own work in accomplishing the invention. Especially, the paragraph at page 1, line 23 to page 2, line 4 describes the inventors' own work in defining the problem that the invention solves.

3. I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code

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Application Serial No. 10/046,741
Attorney Docket No. 3885-0103P

and such willful false statements may jeopardize the validity of the application or
any patent issuing thereon.

July 2, 2004
Date

By Yoshinobu Ono
Yoshinobu Ono